

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#2
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8-1981

In re U.S. Patent Application of)
WATANABE et al.)
Application Number: To be assigned)
Filed: Concurrently herewith)
For: SIGNAL PROCESSING CIRCUIT)

JCS68 U.S. PRO
09/870630
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**Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231**

INFORMATION DISCLOSURE STATEMENT

Sir:

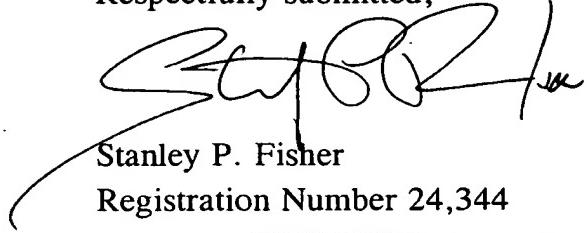
Pursuant to 37 C.F.R. §§ 1.51(b) and 1.56, this Information Disclosure Statement is submitted in the above-identified patent application. A listing of documents to be published on the face of any patent granted from this application is submitted herewith on Form PTO-1449. Any other documents or information submitted for consideration by the Examiner are listed in this paper. A copy of each U.S. and foreign patent, or each publication or portion thereof listed or herein identified, is submitted herewith.

This Information Disclosure Statement is submitted prior to the mailing of the first Office Action in the above-captioned U.S. patent application. Accordingly, no fee is due or payable at this time.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

Please charge any additional fees or credit any overpayments in connection with
this paper to Deposit Account No. 08-1480.

Respectfully submitted,



Stanley P. Fisher
Registration Number 24,344

JUAN CARLOS A. MARQUEZ
Registration No. 34,072

REED SMITH HAZEL & THOMAS LLP
3110 Fairview Park Drive
Suite 1400
Falls Church, Virginia 22042
(703) 641-4200

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